SWE CHIEFE

USN		EE56
	1	Fifth Semester B.E. Degree Examination, Dec.09/Jan.10
-50		Operational Amplifiers and Linear IC's
Tim	ne: 3	hrs. Max. Marks:100
		Note: 1. Answer any FIVE full questions.
		2. Use of resistor and capacitor standard values list and Op-Amp data sheets are permitted.
		3. Any missing data may be suitably assumed.
	26	
1	a.	With a neat circuit diagram, explain the operation of capacitor coupled voltage follower
		(UO IVIATRS)
	b.	With a neat sketch, briefly explain the circuit of a capacitor coupled inverting operational amplifier using a single polarity supply. (04 Marks)
8	c	Thing I E252 RIFET On amp design a high Zin capacitor coupled non-inverting amplifier
	O.	to have a low cutoff frequency of 120 Hz. The input and output voltages are to be 10 MV
	4	and 2 V respectively and the minimum load resistance is $12 \text{ k}\Omega$. (08 Marks)
•		Sketch circuits for lag compensation, lead compensation and Miller-effect compensation.
2	a.	Explain the operation of each circuit, and how each affects the op-amp frequency response.
		(12 Marks)
	b.	i) Calculate the cutoff frequency-limited rise time for a voltage follower circuit using a 741 op-amp. Also determine the slew rate-limited rise time if the output amplitude is
		to he 5 V
		ii) Determine the maximum undistorted pulse output amplitude for the 741 voltage
		follower if the output rise time is not to exceed I US.
		iii) Calculate the minimum output rise time and the maximum pulse amplitude at that rise time for a 741 amplifier with an upper cutoff frequency of 100 kHz. (08 Marks)
		·
3	a.	With a neat circuit diagram, explain the operation of high input impedance full wave
		precision rectifier. Draw the voltage waveforms throughout the circuit and write the
	L	appropriate equations to show that turn wave recent the gional control and output voltage
	b.	waveforms. Explain the circuit operation. (06 Marks)
	c.	For voltage follower type neak detector, the pulse type signal voltage has a peak value of
		and the output voltage is to be need at 3 v for a
	×	time of 120 µs. The maximum output error is to be approximately 1%. Calculate the required component values and specify the output current and slew rate of the operational amplifiers.
		(06 Marks)
		Description on inverting Schmitt trigger circuit and explain its operation. (06 Marks)
4	a.	Draw an op-amp non-inverting seminituring of children and write the equations
	b.	Discuss the design process for an op-amp monotone and a contraction (06 Marks)

c. Design an astable multivibrator using a BIFET op-amp to have a ±9 V output with a

for calculating each component value.

frequency of 1 kHz.

(06 Marks)

(08 Marks)

- What is an oscillator? What are the conditions to get sustained oscillation? Differentiate 5 (06 Marks) amplifier and oscillator.
 - Discuss the operation of wein bridge oscillator using op-amp with neat circuit diagram. b.

(06 Marks)

- Design the phase shift oscillator with amplitude stabilization network to give a maximum output of ±3 V with an oscillation frequency of 6 kHz. Include distortion minimization (08 Marks) adjustment.
- What is an active filter? How it differs from passive filter.

(04 Marks)

Derive the equation for phase shift in an all pass phase lag network.

(08 Marks)

- Design an all pass circuit using op-amp 741 to have a phase lag adjustable from 80° to 100°. The input signal has a 1 V amplitude and a 5 kHz frequency. (08 Marks)
- What is switched-capacitor filter? Discuss the theory of operation of switched-capacitor 7 (10 Marks) filter. (04 Marks)
 - What is a phase locked loop? Define the term lock range and capture range.

Discuss with block diagram, a frequency multiplier using PLL.

(06 Marks)

Sketch the circuit of a tracking plus-minus supply. Explain its operation. 8

(06 Marks)

b. Discuss fold-back current limiting circuit.

(06 Marks)

c. Design a positive voltage regulator using IC723 to have an output of 18 V. Determine suitable resistance values, select an appropriate input voltage and determine the maximum (08 Marks) load current that may be supplied.